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PATENT APPLICATION

#15
283-9-00
Robert
Letter

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shigeru ATSUMI

Serial No. 09/028,276

Filed: February 24, 1998

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND FLASH
EEPROM



Group Art Unit: 2815

Examiner: Jesse A. Fenty

Atty Dkt No. 1701.73982

REQUEST FOR RECONSIDERATION UNDER 37 CFR § 1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This paper is responsive to the Office Action mailed December 9, 1999 and is filed prior to the expiration of the three-month statutory period for response set to expire on March 9, 2000. Accordingly, it is believed that no fee is due in connection with this filing. In the event that a fee is due, please charge our deposit account no. 19-0733.

REMARKS

Reconsideration and allowance of the instant application are requested. Claims 1-42 remain pending. Claims 10-12 and 15-20 have been withdrawn from consideration, but may be reconsidered upon allowance of a generic claim under 37 CFR § 1.141.

Preliminarily, applicant wishes to thank the Examiner for the courtesies extended to the undersigned during the telephonic interviews of February 9, 15 and 22.